

Micron engineering candidate guide

Micron is looking for the brightest minds to join our international team of innovators.

Department	Job title(s)	Education	Degree	Preferred coursework	Job location(s)	
Process/Equipment R&D Development	<ul style="list-style-type: none"> Development Engineer R&D Process Engineer Processes: Photolithography, Dry Etch, Wet Chemistry, Thin Films, CMP, Meteorology, etc. 	<ul style="list-style-type: none"> MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical Engineering Electrical Engineering Mechanical Engineering Microelectronics Engineering Materials Science Physics 	<ul style="list-style-type: none"> Computational Fluid Dynamics Design of Experiment Device or Processing Research Semi Process Lab 	<ul style="list-style-type: none"> SPC Statistics Thermo-mechanical Modeling Transport Phenomena 	<ul style="list-style-type: none"> Boise, ID
Process R&D Integration	<ul style="list-style-type: none"> Process Integration Engineer 	<ul style="list-style-type: none"> MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical Engineering Electrical Engineering Materials Science Physics 	<ul style="list-style-type: none"> Device or Processing Research CMOS DOE – SPC, JMP 	<ul style="list-style-type: none"> Semiconductor Physics Magnetics Device Work Circuit Design 	<ul style="list-style-type: none"> Boise, ID
Mask Technology Center	<ul style="list-style-type: none"> Data Science Engineer Reticle Manufacturing Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical Engineering Mechanical Engineering Materials Science Mathematics/Statistics Physics 	<ul style="list-style-type: none"> Semiconductor Physics C++, SQL, Java, Python 	<ul style="list-style-type: none"> Boise, ID 	
Central Engineering	<ul style="list-style-type: none"> CAD Engineer Competitor Analysis Engineer EMI/EMC Engineer Lab Engineer PCB Design and Test Engineer Signal Integrity Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical Engineering Computer Engineering Electrical Engineering Mechanical Engineering Thermal Engineering EHSQ Materials Science Physics 	<ul style="list-style-type: none"> Device physics EDA Electromagnetics Electron Microscopy Lab testing equipment Mechanical Understanding of Tools 	<ul style="list-style-type: none"> PCB Design VLSI Design Programming Statistics 	<ul style="list-style-type: none"> Atlanta, GA Austin, TX Dallas, TX Boise, ID Folsom, CA San Jose, CA Longmont, CO Minneapolis, MN
Quality	<ul style="list-style-type: none"> Manufacturing Quality Engineer Quality Data Analyst Engineer Quality Product Reliability Engineer 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering Mechanical Engineering 	<ul style="list-style-type: none"> CMOS Logic Manufacturing Project Management 	<ul style="list-style-type: none"> Semiconductor Physics Statistics: C++ VLSI Design 	<ul style="list-style-type: none"> Boise, ID Longmont, CO

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Test and Probe	<ul style="list-style-type: none"> Field Service Engineer Test Engineer 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering 	<ul style="list-style-type: none"> Digital Logic Electrical/Electronics 	<ul style="list-style-type: none"> Programming (C++ and Unix preferred) Boise, ID Manassas, VA
SSD Engineering	<ul style="list-style-type: none"> IC Design Engineer Verification/Validation Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering 	<ul style="list-style-type: none"> Computer Architecture Computer Science Digital Logic Design 	<ul style="list-style-type: none"> Verilog/VHDL/SystemVerilog VLSI Design Boise, ID Folsom, CA San Jose, CA Longmont, CO
DRAM Engineering	<ul style="list-style-type: none"> Design Engineer Product Engineer Verification/Validation Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering 	<ul style="list-style-type: none"> C# C++ CMOS Logic 	<ul style="list-style-type: none"> Digital Logic Semiconductor Physics VLSI Design Atlanta, GA Boise, ID Dallas, TX Folsom, CA
Firmware Development	<ul style="list-style-type: none"> Firmware Architect Firmware Developer Firmware Test Architect Firmware Tester 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering 	<ul style="list-style-type: none"> Computer Architectures Control Theory Digital Design Digital Devices 	<ul style="list-style-type: none"> Digital Signal Processing Programming projects in C/C++ Real Time Systems Boise, ID Folsom, CA San Jose, CA Longmont, CO Minneapolis, MN
Information Technology	<ul style="list-style-type: none"> Business Analyst Database Administrator Project Manager Software Engineer System Manager 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering Computer Information Systems Management Information Systems Computer Science 	<ul style="list-style-type: none"> C#/C++ OOP/OOD – Unix, Linux Perl – Java SAP – ABAP – SSIS – SSRS 	<ul style="list-style-type: none"> SQL Server – NET SQL Windows – Oracle, MS-SQL Boise, ID
Micron Technology High Volume Fab	<ul style="list-style-type: none"> EHS Engineer Equipment Engineer Fab Engineer Facilities Engineer Manufacturing Engineer Process Integration Engineer 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Chemistry/Chemical Engineering Electrical Engineering Environmental Engineering Industrial Engineering Mechanical Engineering Microelectronics Engineering Materials Science Physics 	<ul style="list-style-type: none"> Cleanroom Device Knowledge MAT LAB Mechanical Understanding of Tools PC Skills 	<ul style="list-style-type: none"> Programming Semi Process Lab Semiconductor Materials Semiconductor Physics Statistics Boise, ID Clay, NY Manassas, VA
Nonvolatile Engineering Group	<ul style="list-style-type: none"> Characterization Engineer Product Engineer Design Engineer Verification Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Computer Engineering Electrical Engineering 	<ul style="list-style-type: none"> Analog Circuits CMOS Logic Digital Logic Programming Language: (C++, C#, Java, etc.) 	<ul style="list-style-type: none"> Semiconductor Physics VLSI Design Boise, ID Folsom, CA San Jose, CA Longmont, CO

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